



PATENT
Attorney Docket No.: SAM-0550

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Nam-Jung Her, *et al.*
Serial No.: 10/802,472
Filing Date: March 16, 2004
Title: SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING TEST PINS AND
METHOD FOR TESTING THEREOF

Examiner: Patel, Paresh H.
Group Art Unit: 2829

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

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12-14-05

Date


Stacy Sedlak

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT A

Sir:

The following is in response to the Office Action dated September 14, 2005, and is filed within the shortened statutory period of three months. Please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.